

15TH INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING

December 17-20, 2008 | Bangalore, INDIA **SCHEDULE**

SCHEDULE	WEDNESDAY 17 DEC				THURSDAY 18 DEC			DEC	FRIDAY 19 DEC	SATURDAY 20 DEC
7:00am - 7:30am					Breakfast -					
7:30am - 8:00am	- Breakfast								Breakfast	Breakfast
8:00am - 8:30am					Opening Remarks				Breaklast	BreakidSt
8:30am - 9:00am		Workshop 2 Vijayanagar II	Workshop 3 Trinity II	Student Research Symposium Trinity I	Keynote Address I			ec I	Keynote Address III	Keynote Address IV
9:00am - 9:30am	Workshop 1 Vijayanagar I							33 1	Reynote Address III	Reynote Address IV
9:30am - 10:00am					Break				Break	Break
10:00am - 10:30am					-	V l noi	y on II	=		
10:30am - 11:00am	Break				sssion	Industr Ity II Industr n Session				
11:00am - 11:30am		_ =	e	Student Research Symposium Trinity I	Technical Session I	User & Industry Symposjum Session I Trinity II User & Industry Symposjum Session II	Jser & Iposiun Vijayar		Technical Session III	Technical Session VI
11:30am - 12:00	Workshop 1 Vijayanagar I	Workshop 2 Vijayanagar II	Workshop 3 Trinity II				Sym			
12:00 - 12:30pm										
12:30pm - 1:00pm					1.	unch		gar I]	Lunch	Lunch
1:00pm - 1:30pm	Lunch				Lunch (Vijayanagar I)			yana	Lunch	Lunch
1:30pm - 2:00pm	Luncii									
2:00pm - 2:30pm	Workshop 4 Vijayanagar I	Workshop 5 Vijayanagar II	Tutorial 1 Trinity I	Tutorial 2 Trinity II	Reynote Address II		33 II	Demos		Technical Session VII
2:30pm - 3:00pm					Technical Session II	User & Industry Symposium Session III Trinity III		ۍ Del	Technical Session IV	reclinical Session vii
3:00pm - 3:30pm							User & Industry Symposium Session IV Vijayanagar II	Exhibits &		
3:30pm - 4:00pm					cal Se					Break
4:00pm - 4:30pm		Break			echni	Jser & Trin Trin Jser & P	Jser & posiun Vijayar	Vijayar	Break	
4:30pm - 5:00pm	Workshop 4 Vijayanagar I	orkshop 5 jayanagar II	Futorial 1 Frinity I	Tutorial 2 Trinity II		Sym	Sym			Technical Session VII
5:00pm - 5:30pm					Break				Technical Session V	reclifical Session vii
5:30pm - 6:00pm		Works Vijaya	Tuto		Industry Panel				recrinical Session v	
6:00pm - 6:30pm										
6:30pm - 7:00pm	Student Research Symposium Reception Trinity I								Break	
7:00pm - 7:30pm										
7:30pm - 8:00pm										
8:oopm - 8:3opm	Worksho	p II: Works	ervice-Orie	ty Computing ted Engineering and Optimizations				Conference Banquet		
8:30pm - 9:00pm	Workshop III: High Performance FPGA/Reconfigurable Computing Workshop IV: Workshop on Next Generation Wireless Networks Workshop V: Cooling of HiPC Systems: Why Computer Scientists Should Care? Tutorial I: High Performance Computing with CUDA									
9:00pm - 9:30pm	Tutorial Tutorial	I: High Perl	formance - Deliverir	Computing	with CUDA Scale Com					

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- > Registration desk will be open from 7:00 am to 4:00 pm from Wednesday to Saturday (December 17 20).
- > Conference registration fee includes breakfast, lunch, and refreshments from December 17 20, and banquet on December 19.
- > Conference site : Taj Residency, Bangalore, India.
- > All technical sessions and keynote addresses of the main conference will be in Trinity I, unless otherwise noted.